

LSI LOGIC

AUG 08 2005

FAX

LSI Logic Corporation  
Legal Department - IP  
1621 Barber Lane, MS D-106  
Milpitas, CA 95035Date August 8, 2005Number of pages including cover sheet 2

To: US PTO

Mail Stop Status Request

TC 2188

Fax No. 571-273-8300

Phone No.

CC:

From: Connie del Castillo

Intellectual Property Paralegal

Telephone No. (408) 433-7191

Fax No. (408) 433-7460

## REMARKS:

☐ Urgent☐ For your review☒ Reply ASAP☐ Please commentApplication Number: 10/692,664  
Filing date: October 23, 2003  
First named inventor: Alexandre Andreev et al.  
Attorney docket number: 03-1496

Transmitted herewith for filing via facsimile:

- Status Request Letter

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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AUG 08 2005

In re Application of :

Alexandre Andreev et. al.

Serial No. :

10/692,664

Filed :

October 23, 2003

For :FIFO Memory With Single Port  
Memory Modules For Allowing  
Simultaneous Read And Write  
OperationsGroup Art Unit :

2188

Examiner :Atty Docket :

/ 03-1496

STATUS REQUESTCommissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

LSI Logic Corporation  
1621 Barber Lane, MS D-106  
Milipitas, CA 95035  
[+1] 408-433-7191Date: 8 Aug 05

Respectfully submitted,



Timothy Croll, Ph: [+1] 408-433-7191

Reg. No. 36,771